



Virtual University

About Us

CS401
Solved Final Term Paper 6

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Year
2017

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بِسْمِ اللَّهِ الرَّحْمَنِ الرَّحِيمِ

In the Name of Allāh, the Most Gracious, the Most Merciful

Paper Pattern

MCQS 40 each 1 mark
Short 4 each 2 marks
Short 4 each 3 marks
long 4 each 5 marks

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of "PUSH AX" statement

Answer (Please select your correct option)

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☐

AX register will reside on the stack

☒

A copy of AX will go on the stack

☐

The value of AX disappear after moving on stack

☐

Stack will send an acceptance message

Made by: Waqar Siddhu

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

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☒

IRQ 4

☐

IRQ 5

☐

IRQ 6

☐

IRQ 7

Made by: Waqar Siddhu

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

An End of Interrupt (EOI) signal is sent by

Answer (Please select your correct option)

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☒ Handler

☐ Processor

☐ IRQ

☐ PIC

Made by: Waqar Siddhu

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

The programmable interval timer (PIT) has input frequency of

Answer (Please select your correct option)

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☒ 1.193MHZ

☐ 2.193MHZ

☐ 3.193MHZ

☐ 4.193MHZ

Made by: Waqar Siddhu

Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

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☐ Key board interrupt

☒ Timer interrupt

☐ INT 2

☐ INT 3

Made by: Waqar Siddhu

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

The actual physical address represented by the segment-offset pair "AB01:FFFF" will be:

Answer (Please select your correct option)

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☐ 1AB00

☐ 001AB

☒ BB00F

☐ F00BB

Made by: Waqar Siddhu

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

Suppose DH contains 10001010 and CL contains 3, what is the value of DH after the following instruction:
SHL DH, CL

Answer (Please select your correct option)

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☒ 01010000

☐ 10001010

☐ 00010100

☐ 01010111

Made by: Waqar Siddhu

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

When two devices in the system want to use the same IRQ line then what will happen?

Answer (Please select your correct option)

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☐ An IRQ Collision

☒ An IRQ Conflict

☐ An IRQ Crash

☐ An IRQ Blockage

Made by: Waqar Siddhu

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of STOSW, the CX will be

Answer (Please select your correct option)

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☒ Decrement by 1

☐ Decrement by 2

☐ Increment by 1

☐ Increment by 2

Made by: Waqar Siddhu

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following is true ?

Answer (Please select your correct option)

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☒ In general the memory cell cannot be wider than data bus

☐ Control bus is unidirectional

☐ Memory can use the address bus to refer to certain register in processor

☐ There is no relation between memory and data bus

Made by: Waqar Siddhu

Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

Traditionally all the mathematical and logical operations are performed in _____

Answer (Please select your correct option)

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☐ Code Segment register

☐ Data Segment register

☒ Accumulator register

☐ Stack segment register

Made by: Waqar Siddhu

Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

Relating to the stack in 8088 architecture which of the following statement is true ?

Answer (Please select your correct option)

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☐

Single word can not be pushed on the stack

☐

Single word can not be popped from the stack

☒

Single byte can not be popped from the stack

☐

Single quad can be popped from the stack

Made by: Waqar Siddhu

Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

The second byte of the word designated for one screen location holds

Answer (Please select your correct option)

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☐

The dimensions of the screen

☐

Character position on the screen

☒

Character color on the screen

☐

ASCII code of the character

Made by: Waqar Siddhu

Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

INT 10 is used for _____ services.

Answer (Please select your correct option)

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☒

BIOS video

☐

DOS video

☐

RAM

☐

Disk

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Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

In graphics mode a location in video memory corresponds to a _____ on the screen.

Answer (Please select your correct option)

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- ☐ line
- ☒ dot
- ☐ circle
- ☐ rectangle

Made by: Waqar Siddhu

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

The value of AH register in the write Graphics pixel service is _____

Answer (Please select your correct option)

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- ☒ 0Ch
- ☐ 0Bh
- ☐ 1Ch
- ☐ 2Ch

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Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

The space where the current state of a task is stored is called _____

Answer (Please select your correct option)

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- ☐ control block
- ☒ Process control block
- ☐ stack
- ☐ hard disk

Made by: Waqar Siddhu

Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

Answer (Please select your correct option)

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☒ a)

☐ b)

☐ c)

☐ d)

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Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA VBE 2.0 is a standard for _____

Answer (Please select your correct option)

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☐ Ultra resolution mode

☒ High resolution mode

☐ Medium resolution mode

☐ Low resolution mode

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Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

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☐ Variable Electronics Standards Association

☒ Video Electronics Standards Association

☐ Video Energy Standards Association

☐ Vega Energy Standards Association

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Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

VBE stands for _____, as proposed by VESA (an organization).

Answer (Please select your correct option)

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☒ Video BIOS Extensions

☐ Video BIOS Emergence

☐ Video Binary Extensions

☐ Video Blocked Extensions

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Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

IDTR is a _____ register.

Answer (Please select your correct option)

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☐ 16 bit

☐ 20 bit

☐ 32 bit

☒ 48 bit

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Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

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☐ Programming mode

☒ Real time mode

☐ Visualization mode

☐ Memory mode

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Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

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☐ First Byte

☒ Second Byte

☐ Third Byte

☐ Last Byte

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Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

What is the size of a Sector of a standard 1.44MB Floppy disk having 80 tracks and 18 Sectors/track?

Answer (Please select your correct option)

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☐ 128 Bytes

☐ 256 Bytes

☒ 512 Bytes

☐ 1024 Bytes

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Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer (Please select your correct option)

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☐ INT 10

☐ INT 09

☒ INT 08

☐ INT 07

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Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

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☐ global

☒ local

☐ legal

☐ illegal

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Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

Technically LAPX88 stands for " _____ Advanced Processor Extensions 88."

Answer (Please select your correct option)

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☒ Intel

☐ Integrated

☐ Intra

☐ Internal

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Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

In Left-Shift-Operation, the left most bit _____

Answer (Please select your correct option)

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☐ will be dropped

☒ will go into CF

☐ will be moved to the right-most position

☐ will always be 1

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Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

The serial port connection is a ----- connector

Answer (Please select your correct option)

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☒ 9 pin DB-9

☐ 8 pin DB-9

☐ 3 pin DB-9

☐ 9 pin DB-5

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Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

Answer (Please select your correct option)

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☐ 6

☐ 7

☒ 8

☐ 9

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Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CD (Carrier Detect) ?

Answer (Please select your correct option)

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☒ 1

☐ 2

☐ 3

☐ 4

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Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following BIOS INT provides serial port services

Answer (Please select your correct option)

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☒ INT14

☐ INT11

☐ INT12

☐ INT13

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Question No : 34 of 52

Marks: 1 (Budgeted Time 1 Min)

8088 is a

Answer (Please select your correct option)

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☒ 16 bit processor

☐ 32 bit processor

☐ 64 bit processor

☐ 128 bit processor

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Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

A 32 bit address register can access up to of memory, so memory access has increased a lot.

Answer (Please select your correct option)

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☐ 2GB

☒ 4GB

☐ 6GB

☐ 8GB

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Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

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- ☐ 1
- ☒ 0
- ☐ -1
- ☐ -2

Made by: Waqar Siddhu

Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

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- ☐ 8 bit
- ☐ 16 bit
- ☒ 32 bit
- ☐ 64 bit

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Question No : 38 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a 32 bit that holds the address of currently executing instruction.

Answer (Please select your correct option)

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- ☒ Program counter
- ☐ Stack pointer
- ☐ Register
- ☐ Stack

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Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

Programmable Interrupt Controller (PIC) has

Answer (Please select your correct option)

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☐

One input signals and eight output signals

☐

One input signal and one output signal

☒

Eight input signals and one output signals

☐

Eight input signals and eight output signals

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Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is used in debugging with the help of trap flag?

Answer (Please select your correct option)

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☐

INT 0

☒

INT 1

☐

INT 2

☐

INT 3

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Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

For which purpose INT 1 is used and describe the functionality of this interrupt?

Answer (Please [click here](#) to Add Answer)

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Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

What are different register settings for file closing using INT 21 service?

Answer ([Please click here to Add Answer](#))

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Normal Arial 12 B I U

Made by: Waqar Siddhu

Question No : 43 of 52

Marks: 2 (Budgeted Time 4 Min)

In C language, what naming convention is used to call a function ?

Answer ([Please click here to Add Answer](#))

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Normal Arial 12 B I U

c prepends an underscore to every one

Made by: Waqar Siddhu

Question No : 44 of 52

Marks: 2 (Budgeted Time 4 Min)

Write a single instruction that can be used as an alternative of the following two instructions?

DEC CX
JNZ NextChar

Answer ([Please click here to Add Answer](#))

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Question No : 45 of 52

Marks: 3 (Budgeted Time 6 Min)

Suppose we want to execute multiple processes at same time. For this purpose we need to store and restore states of processes running in a multitasking operating system. Which process is used to store and restore states of processes in a multitasking operating system.

Answer ([Please click here to Add Answer](#))

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Made by: Waqar Siddhu

Question No : 46 of 52

Marks: 3 (Budgeted Time 6 Min)

Consider the function "`int subtract (int x, int y)`" declared in C, Write the code to call this function from Assembly language.

Answer ([Please click here to Add Answer](#))

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Made by: Waqar Siddhu

Question No : 47 of 52

Marks: 3 (Budgeted Time 6 Min)

For what purpose Global Descriptor Table (GDT) is used?

Answer ([Please click here to Add Answer](#))

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Made by: Waqar Siddhu

Question No : 48 of 52

Marks: 3 (Budgeted Time 6 Min)

Check these instructions, whether they are valid or not, if not why? Make corrections and also mention how many bits have been moved in each instruction?

```
mov ax, [num1]
mov al, [num1]
mov ax, al
mov [num1], 5
```

Answer ([Please click here to Add Answer](#))

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Made by: Waqar Siddhu

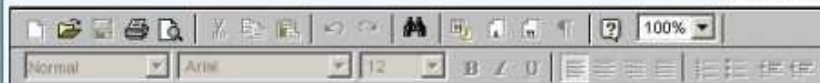
Question No : 48 of 52

Marks: 3 (Budgeted Time 6 Min)

```
mov ax, [num1]
mov al, [num1]
mov ax, al
mov [num1], 5
mov byte[num1], 5
```

Answer ([Please click here to Add Answer](#))

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Made by: Waqar Siddhu

Question No : 49 of 52

Marks: 5 (Budgeted Time 10 Min)

Which processor's instruction set allows the following instructions and describe what task will be performed by each of these instructions?

```
CLR (A3)
EXG D0, D2
MOVEB (A1), (A2)
MOVEA (2222) L, A4
```

Answer ([Please click here to Add Answer](#))

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Made by: Waqar Siddhu

Question No : 50 of 52

Marks: 5 (Budgeted Time 10 Min)

What is the effective address generated by each of the following instruction?
Initially BX=0x0100, label=0x0234, [label]=0x0010, and SI=0x00E1

- a. `mov ax, [bx+40]`
- b. `mov ax, [bx+label]`

Answer ([Please click here to Add Answer](#))

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Made by: Waqar Siddhu

Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

In INT 10 of video services while writing graphics pixel, what do the values of registers BH, AL, CX and DX correspond to?

Answer ([Please click here to Add Answer](#))

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Made by: Waqar Siddhu

Question No : 52 of 52

Marks: 5 (Budgeted Time 10 Min)

What are different register settings for memory allocation using INT 21 Service?

Answer ([Please click here to Add Answer](#))

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